



## 20G-PSDD-4

**20G phase shifter with integrated Doubler & Driver**

### Description

The **20G-PSDD-4** is an analog phase shifter with integrated frequency doubler and variable gain stage with output power amplifier in SMD package.

For an input frequency of 10,7GHz the device delivers an output signal at 21,4 GHz.

The device is capable of up to 800° phase shift. The output amplifier is designed to deliver 4Vpp with low distortion (also included output a filter). The variable gain stage has a wide dynamic range of 18dB.

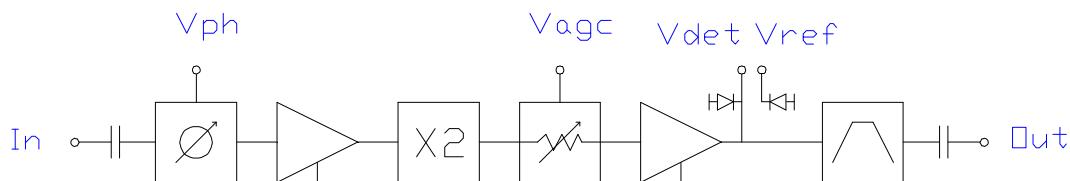
A power detector, with reference diode, is also included, giving a direct measurement of the output power.

### Applications

- Phase control
- Radar
- Fiber transmission
- DPSK
- 20GBps, 40GBps

### Features

- 16x16 mm<sup>2</sup> SMD
- 48 pins w/1mm pitch
- 50Ω RF Single ended input and output
- RF input and output are AC coupled
- Input frequency band : 9.9 – 11.5 GHz
- Output frequency band : 19.8 – 23 GHz
- Wide phase shift from 0 to >540°(up to 800°)
- Low power
- +9V and 5V voltage supply
- Output level, up to 4Vpp
- Wide dynamic gain control 18dB
- Phase shift command
- Gain command
- Output level detection
- Ref diode output



**ANALOG PHASE SHIFTER DOUBLER FUNCTIONNAL BLOCK DIAGRAM**

**Typical Characteristics** (ambiant 25°C on heat sink otherwise stated)

<b>Parameter</b>	<b>Symbol</b>	<b>Conditions</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>	<b>Comment</b>
Positive supply voltage 1	VDD1		8.55	9	9.45	V	
Positive supply voltage 2	VDD2		8.55	9	9.45	V	
Positive supply voltage 3	Vcc		4.75	5	5.25	V	
Positive supply current 1	IDD1	VDD1	150	200	250	mA	
Positive supply current 2	IDD2	VDD2	150	200	250	mA	
Positive supply current 3	Icc	Vcc		100	120	mA	
Input frequency	F		9.95	10.709	11.5	GHz	
Input impedance adaptation	S11	50 Ohm			-10	dB	
Output impedance adaptation	S22	50 Ohm			-8	dB	
Input amplitude	Vin		300		1 000	mVpp	
Output amplitude max	Voutmax	With AGC	4			Vpp	
AGC amplitude control voltage	Vagc	Vout and Vin from Min to Max	-4		4	V	
AGC gain slope	Sagc	Monotonic				Vpp/V	TBD
AGC input impedance	Zagc					Ohm	TBD
Min Max output controlled phase delay	Ph delay Max	Vph=10V @ 21.4 GHz	60	90		ps	
Phase delay control voltage	Vph		0		10	V	
Phase delay control slope	Sph			9		ps/V	
Vph input impedance	Zph					Ohm	TBD
Input signal at output	H0	10.7 GHz		-30	-20	dB	
Third harmonic	H3	32.1 GHz		-30	-20	dB	
Power detector output voltage	Vdet	Vout = Max		500		mV	TBC
Output voltage variation with phase delay control	ΔVout(Δph)	Vph from Min to Max				dBpp	TBD
Phase delay variation with temperature	ΔPh delay(ΔT)	Input and controls = constants				ps/°C	TBD
Phase delay variation with gain control	ΔPhdelay(ΔG)	Vout from Min to Max				ps	TBD

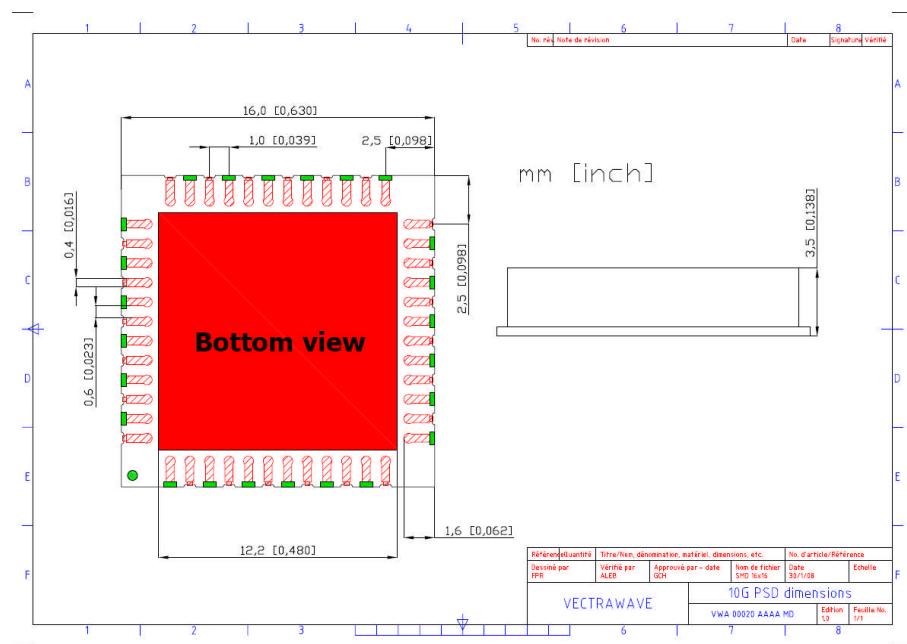
<b>Environment Parameters</b>		<b>Symbols</b>	<b>Min</b>	<b>Max</b>	<b>Units</b>
Operating temperature	Case (bottom)	T <sub>op</sub>	-5	+75	°C
Storage temperature	Case (bottom)	T <sub>stg</sub>	- 40	+85	°C

**Absolute maximum ratings**

<b>Maximum ratings</b>	<b>Symbols</b>	<b>Min</b>	<b>Max</b>	<b>Units</b>
Positive supply voltage 1,2	VDD <sub>max</sub>		+10	V
Positive supply voltage 3	VCC <sub>max</sub>		+6	V
Storage temperature - Case (bottom)-	T <sub>st</sub> <sub>max</sub>		125	°C
Phase delay control voltage command	Vph <sub>max</sub>		+11	V
AGC amplitude control voltage	Vagc <sub>max</sub>	-6	+6	V

## Pin out and pin description

<b>Pad #</b>	<b>Function</b>						
<b>1</b>	Ground	<b>13</b>	Ground	<b>25</b>	Ground	<b>37</b>	Ground
<b>2</b>	Ground	<b>14</b>	Ground	<b>26</b>	Ground	<b>38</b>	Ground
<b>3</b>	Ground	<b>15</b>	Ground	<b>27</b>	Ground	<b>39</b>	Ground
<b>4</b>	Vdd1	<b>16</b>	Vph	<b>28</b>	Output	<b>40</b>	Ground
<b>5</b>	Ground	<b>17</b>	Ground	<b>29</b>	Ground	<b>41</b>	Ground
<b>6</b>	Ground	<b>18</b>	NC	<b>30</b>	Vdd2	<b>42</b>	Vagc
<b>7</b>	Ground	<b>19</b>	Ground	<b>31</b>	Ground	<b>43</b>	Ground
<b>8</b>	Ground	<b>20</b>	NC	<b>32</b>	Vdet	<b>44</b>	Vcc
<b>9</b>	Ground	<b>21</b>	Ground	<b>33</b>	Ground	<b>45</b>	Ground
<b>10</b>	Input	<b>22</b>	Ground	<b>34</b>	Vref	<b>46</b>	Ground
<b>11</b>	Ground	<b>23</b>	Ground	<b>35</b>	Ground	<b>47</b>	Ground
<b>12</b>	Ground	<b>24</b>	Ground	<b>36</b>	Ground	<b>48</b>	Ground



## Handling

This product is sensitive to electrostatic discharge and should not be handled except at a static free workstation. Take precautions to prevent ESD; use wrist straps, grounded work surfaces and recognized anti-static techniques when handling the **20G-PSDD-4** modules.

Care should be taken to avoid supply transient and over voltage. Over voltage above the maximum specified in absolute maximum rating section may cause permanent damage to the device.

## Ordering information

<b>Product code</b>	<b>Name</b>
VWA 00060 AB	20G-PSDD-4 SMD 540°Phase shifter
VWA 00061 AB	Demonstration board equipped w/phase shifter VWA 00060 AA



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**VWA 00060 AB xx DS Rev 0.1**

VectraWave Proprietary information subject to change without notice

October 08

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